

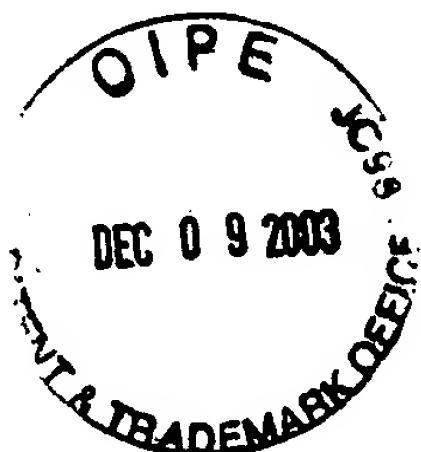
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Complete and send this form, together with applicable fee(s), to: **Mail** Mail Stop ISSUE FEE  
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7590 09/11/2003

JONES VOLENTINE, L.L.P.  
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/883,363	06/19/2001	Ken Ogura	OKI.244	1079

TITLE OF INVENTION: CONDUCTOR POSTS, CONSTRUCTION FOR AND METHOD OF FABRICATING SEMICONDUCTOR INTEGRATED CIRCUIT CHIPS  
USING THE CONDUCTOR POST, AND METHOD OF PROBING SEMICONDUCTOR INTEGRATED CIRCUIT CHIPS

CHIPS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1300 1330	\$300	\$1600 1630	12/11/2003
EXAMINER	ART UNIT	CLASS-SUBCLASS			
NGUYEN, HA T	2812	438-106000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 VOLENTINE FRANCOS, PLLC  
2 \_\_\_\_\_  
3 \_\_\_\_\_

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

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OKI ELECTRIC INDUSTRY CO., LTD.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

TOKYO, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ governm

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(Authorized Signature) ANDREW J. TELESZ, JR. (Date) 12/09/03  
#33,581

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12/11/2003 DENMANU2 00000033 09883363

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**TRANSMITTAL OF PAYMENT OF ISSUE FEE (Large Entity)**  
**(37 C.F.R. 1.311)**

Docket No.  
OKI.244

Applicant(s): Ken OGURA

Serial No.  
09/883,363

Filing Date  
June 19, 2001

Examiner  
Ha T. Nguyen

Group Art Unit  
2812

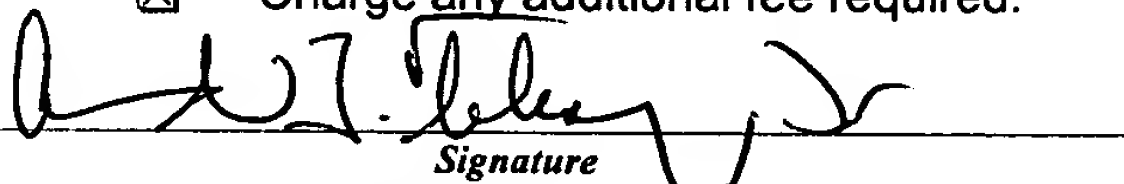
Confirmation No.  
1079

Invention: **CONDUCTOR POSTS, CONSTRUCTION FOR AND METHOD OF FABRICATING SEMICONDUCTOR INTEGRATED CIRCUIT CHIPS USING THE CONDUCTOR POST, AND METHOD OF PROBING SEMICONDUCTOR INTEGRATED CIRCUIT CHIPS**

**Mail Stop Issue Fee**  
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Transmitted herewith are the following for the above-identified application.

- ☒ Issue Fee Transmittal Form PTOL-85
- ☒ Utility Fee: \$ 1330.00    ☐ Design Fee: \_\_\_\_\_    ☐ Plant Fee: \_\_\_\_\_
- ☒ Publication Fee: \$ 300.00
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Dated: December 9, 2003

ANDREW J. TELESZ, JR.  
REG. NO. 33,581

VOLENTINE FRANCOS, P.L.L.C.  
12200 SUNRISE VALLEY DRIVE, SUITE 150  
RESTON, VA 20191

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